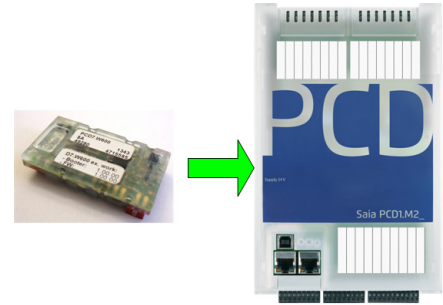


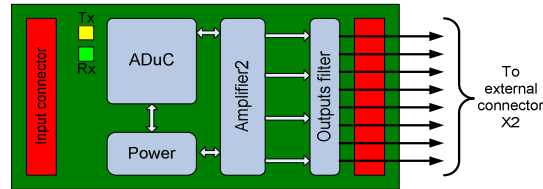
Saia PCD7.W600

4 analogue outputs module
 0..+10V with 12-bits resolution
 for the PCD1 CPU

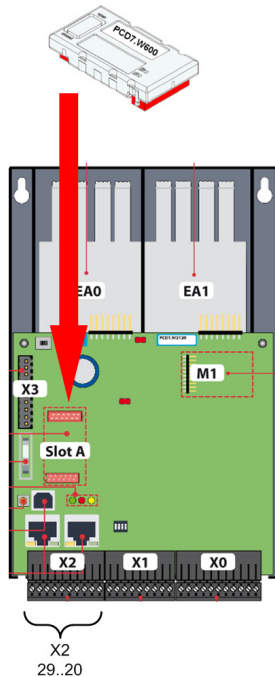


This new analogue outputs (range 0 to +10V) with 12 bits resolution has been especially developed for the new PCD1 CPU (PCD1.M2xxx, PCD1.M0160E0, PCD1.M2110R1). It can be plugged in the slot A instead of a communication interface.

Module overview



Connections



X2 Connector	
20	PGND
21	A0+
22	A0-
23	A1+
24	A1-
25	PGND
26	A2+
27	A2-
28	A3+
29	A3-

Technical data

Module compatibility	PCD1.M2xxx,PCD1.M0160E0,PCD1.M2110R1	
Module current consumption	V+	25mA
	+5V	30mA
Number of outputs	4	
Terminals	1 plug-in screw terminal block, 10-pole, 3.5mm for wiring up to 1mm ²	
Isolation	No isolation between channels and/or PCD	
Output signal range & Resolution	With D/A converter 12 bits	
	Nominal range: 0...+10V Resolution 2.6mV value of least significant bit (LSB)	
Monotonicity	Yes	
Output impedance (max.)	0.7Ω	
Allowable load resistance	≥3KΩ	
Allowable capacitive load	≤20nF	
Allowed types of load	Floating or grounded (the minus of the outputs is internally connected with the PCD ground)	
Short-circuit protection	YES permanent	
Accuracy @25 °C	±0.2% of the full scale (10V)	
Temperature coefficient	±100ppm/K of the full scale (10V)	
Accuracy over full temperature range (0..55 °C)	±0.5% of the full scale (10V)	
Maximum temporary deviation during electrical interference test	±0.2% of the full scale (10V) for fast transient burst (EN 61000-4-4) & for Radiofrequency interference(EN 61000-4-6)	
Settling time for full-range change	≤5ms	
Overshooting	0.1% of the full range (10V)	
Scaling (PG5)	0..4095 , 0..10000 or User defined	

Ordering information

Model	Description	Weight
PCD7.W600	Analogue output module, 4 channels, 12 bits, 0...10 V, to be connected on PCD1 onto slot A	10 g

Saia-Burgess Controls AG
 Bahnhofstrasse 18 | 3280 Murten, Switzerland
 T +41 26 672 72 72 | F +41 26 672 74 99
www.saia-pcd.com

support@saia-pcd.com | www.sbc-support.com